

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,299,029 B2  
APPLICATION NO. : 10/661879  
DATED : November 20, 2007  
INVENTOR(S) : Joseph S. Elder et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page, Item (56)

Page 2, OTHER PUBLICATIONS, insert;

-- Khorram, S. et al., "A CMOS Limiting Amplifier & Logarithmic Signal-Strength Indicator," Integrated Circuits & Systems Laboratory, Electrical Engineering Department, University of California, Los Angeles, 19 pages.

Rofougaran, A., et al., "A 900 MHz CMOS LC-Oscillator with Quadrature Outputs," presented at the 1996 Int'l. Solid-State Circuits Conference, 10 pages.

Rudell, Jacques C., "CMOS VLSI Frequency Translation for Multi-Standard Wireless Communication Transceivers," Qualifying Examination, University of California, Berkeley, April 8, 1996, pp. 1-24.

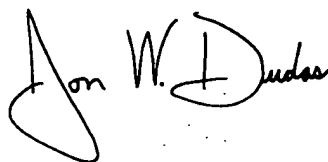
Chang, Glenn et al., "An Agile, Low-Power CMOS Digitally Synthesized 0-13 MHz Sinewave Generator," Integrated Circuits & Systems Laboratory, Electrical Engineering Department, University of California, Los Angeles, 14 pages. --

Column 17, line 6, replace "superhetrodyne" with -- superheterodyne --.

Column 19, line 2, replace "superhetrodyne" with -- superheterodyne --.

Signed and Sealed this

Fourth Day of November, 2008



JON W. DUDAS  
*Director of the United States Patent and Trademark Office*